

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S
INFORMATION
DISCLOSURE STATEMENTAPPLICANT:
William J. Armstrong et al.

(Use several sheets if necessary)

Page 1 of 1

FILING DATE:
August 24, 2001GROUP:
2123

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE (IF APPRO.)
JSP	AA	5 6 5 9 7 8 6	8/19/1997	George et al.	395	653	
JSP	AB	4 8 4 3 5 4 1	6/27/1989	Bean et al.	364	200	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
						YES NO
	AL					
	AM					
	AN					
	AO					
	AP					

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

JSP	AR	U.S. Patent Application Serial No. 09/672,043, entitled "Technique for Configuring Processors in System with Logical Partitions," filed September 29, 2000.
JSP	AS	U.S. Patent Application Serial No. 09/838,057, entitled "Method and Apparatus for Allocating Processor Resources in a Logically Partitioned Computer System," filed April 19, 2001.
JSP	AT	U.S. Patent Application Serial No. 09/939,235, entitled "Yield on Multithreaded Processors," filed August 24, 2001.

EXAMINER

Jason P. [Signature]

DATE CONSIDERED

10/12/2001

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (Modified)

ATTY. DOCKET NO.
ROC920000314US1SERIAL NO.
09/939,232LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S
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BH						
BI						
BJ						
BK						

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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
					YES	NO
BL						
BM						
BN						
BO						
BP						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

BR	IBM Corporation, S/390 Processor Resource/Systems Manager Planning Guide (IBM Pub. No. GA22-7236-04, 5 th Edition, March 1999)
BS	
BT	

EXAMINER

DATE CONSIDERED

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